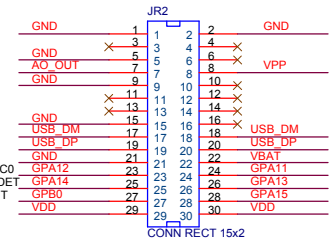


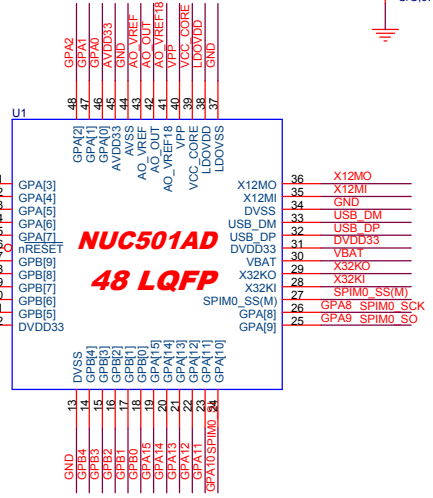
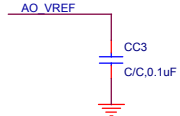
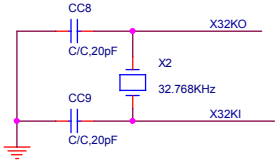
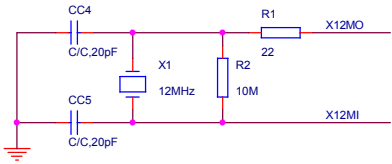
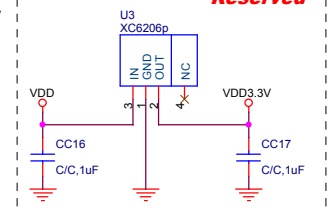
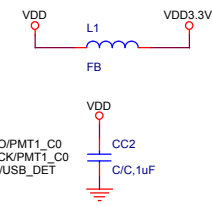
GA12/SPIMS_SI/PMT0_C0
GA14/SPIMS_SS/USB_DET
GPB0/PMT3_C0/USB_DET



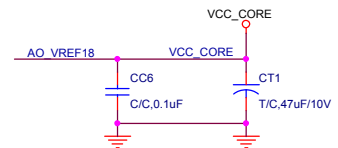
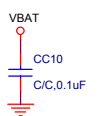
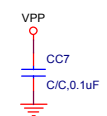
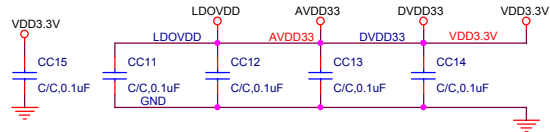
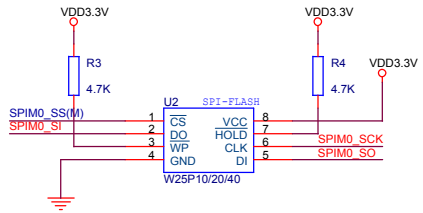
GA11/SPIMS_SO/PMT1_C0
GA13/SPIMS_SCK/PMT1_C0
GA15/PMT2_C0/USB_DET

NUC501AD

Reserved

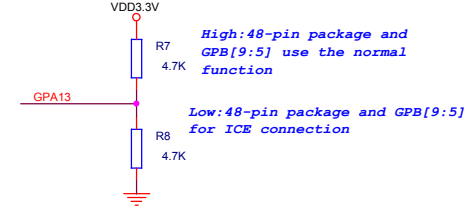


SPIMS(Boot)



Power on setting

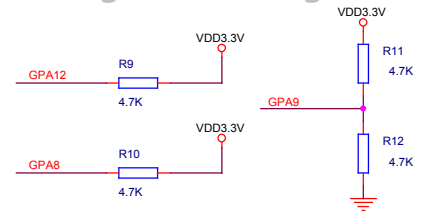
ICE mode configuration setting



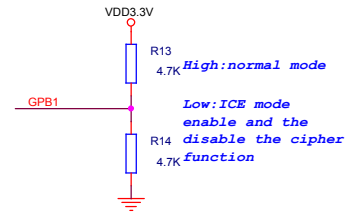
SPIM0 Speed Configuration



Booting Mode setting



ICE Mode configuration setting



GPA12=1 GPA9/GPA8	Function
(0/0)	Boot from SRAM
(0/1)	Boot from USB
(1/0)	OTP program mode
(1/1)	Boot from SPI

nuvoTon

Title NUC501AD		
Size Custom<Doc>	Document Number	Rev A
Date: Friday, March 13, 2009	Sheet 1	of 1